



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Mark G. Johnson et al.

Title: INTEGRATED CIRCUIT INCORPORATING THREE-DIMENSIONAL
MEMORY ARRAY WITH DUAL OPPOSING DECODER
ARRANGEMENT

Application No.: 10/774,818

Filed: February 9, 2004

Examiner: Unknown

Group Art Unit: 2812

Atty. Docket No.: 023-0025

Confirmation No.: 9232

June 24, 2005

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

This amendment is being submitted before the first Office action on the merits.